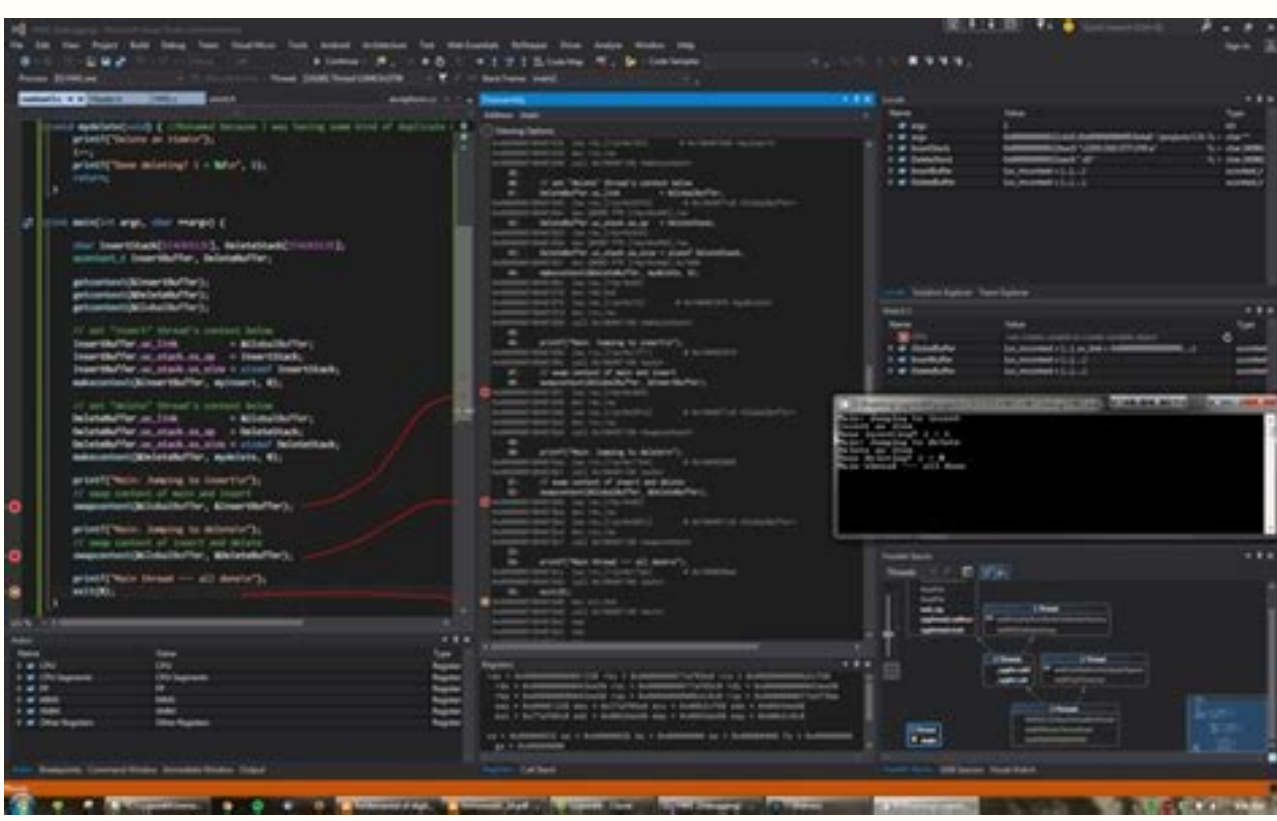




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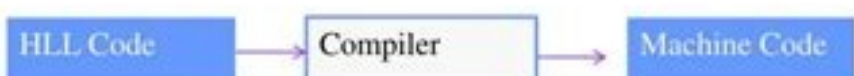


**Open**



Features of High Level Language

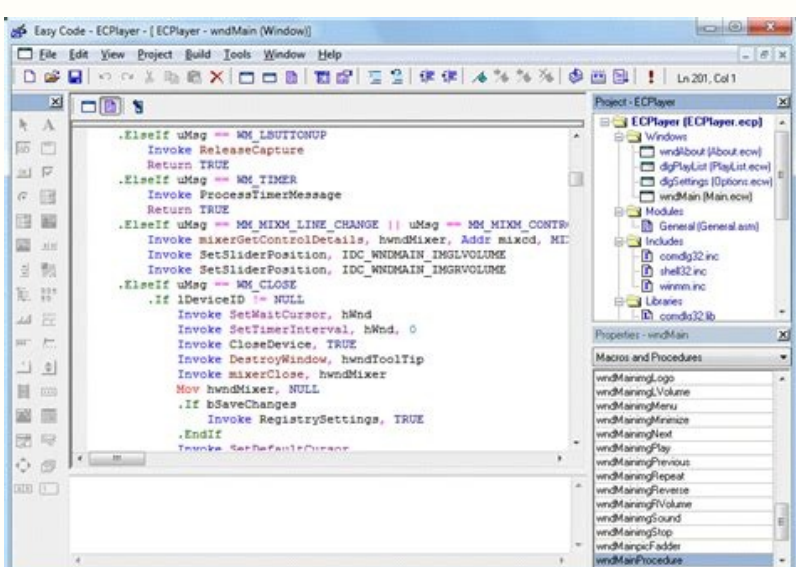
- 1. They have an extensive vocabulary of words, symbols and sentences.
2. Programs are written in the language are translated into many of machine language instructions, this translation are often done by special program called compiler.
3. Libraries of procedures and functions can be incorporated.(Structured programming)
4. A set of rules must be obeyed when writing the program.
5. Instructions in high level language are usually called statements.



High Level Language (HLL)

- Note: these languages introduced in forth generation computer.
Advantages:
1. Easy to learn.
2. Almost Machine Independent.
3. Require less time to write.
4. Provide good documentation.
Disadvantages:
1. Slow execution speed.
2. Occupy more space in memory.

```
main:
; initializes the two numbers and the counter. Note that this assumes
; that the counter and num1 and num2 areas are contiguous!
4:
5: mov ax,'00' ; initialize to all ASCII zeroes
6: mov di,counter ; including the counter
7: mov cx,digits*cntDigits/2 ; two bytes at a time
8: cld ; initialize from low to high memory
9: rep stosw ; write the data
10: inc ax ; make sure ASCII zero is in al
11: mov [num1 + digits - 1],al ; last digit is one
12: mov [num2 + digits - 1],al
13: mov [counter + cntDigits - 1],al
14:
15: jmp .bottom ; done with initialization, so begin
16:
17: .top
18: ; add num1 to num2
19: mov di,num1+digits-1
20: mov si,num2+digits-1
21: mov cx,digits ;
22: call AddNumbers ; num2 += num1
23: mov bp,num2 ;
24: call Printline ;
25: dec dword [term] ; decrement loop counter
26: jmp .top
```



Free assembly language compiler.

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26/2/2021 - UPDATE: This article now has an ARM64 port. Why, in 2021, does anyone need to learn about assembly language? First, reading assembly language is the way to know exactly what your program is doing. Why, exactly, is that C++ program 1 MiB (say) instead of 100 KiB? Is it possible to squeeze some more performance out of that function that gets called all the time? Assembly language syntax. Assembly language uses a mnemonic to represent, e.g., each low-level machine instruction or opcode, each directive, typically also each architectural register, flag, etc.Some of the mnemonics may be built in and some user defined. Many operations require one or more operands in order to form a complete instruction. Most assemblers permit named ... Here you can download the flat assembler - an open source assembly language compiler, packaged for various operating systems. Documentation: All the official documentation for flat assembler, and some other official articles about it are gathered here. Examples in computer programming, machine code is any low-level programming language, consisting of machine language instructions, which are used to control a computer's central processing unit (CPU). Each instruction causes the CPU to perform a very specific task, such as a load, a store, a jump, or an arithmetic logic unit (ALU) operation on one or more units of data in the CPU's ... MIPS is a modular architecture supporting up to four coprocessors (CP0/1/2/3). In MIPS terminology, CP0 is the System Control Coprocessor (an essential part of the processor that is implementation-defined in MIPS I-V), CP1 is an optional floating-point unit (FPU) and CP2/3 are optional implementation-defined coprocessors (MIPS III removed CP3 and reused its opcodes ...)





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